

2.5A, 3MHz Switching Charger with Dynamic Power Path in 8-pin ESOP

DESCRIPTION

ETA6002 is a switching Li-Ion battery charger with dynamic power-path control and input current limiting.

When a battery is connected, depending on the battery voltage, the DC-DC switching regulator either pre-conditions, fast-charges the battery or just regulates a system voltage (V_{SYS}) to a preset voltage. It does not require an external sense resistor for current sensing. The fast-charging current

is determined by programming ISET pin. When the battery voltage reaches the termination voltage i.e. 4.2V, the charging path disconnects SYS to BATT. The ETA6002 also includes a dynamic power path when the SYS load current exceeds current limit of the DCDC regulator internally set, the SYS voltage falls below V_{BATT} , ETA6002 turns on the power-path to supplement the system load through the battery.

FEATURES

- ◆ Switching Charger with Power Path Management
- ◆ Up to 95% DC-DC Efficiency
- ◆ 50mΩ Power Path MOSFET
- ◆ Up to 2.5A Max charging current
- ◆ Instant on with a dead Battery or no Battery
- ◆ No Battery Detection
- ◆ No External Sense Resistor
- ◆ Programmable Charging Current

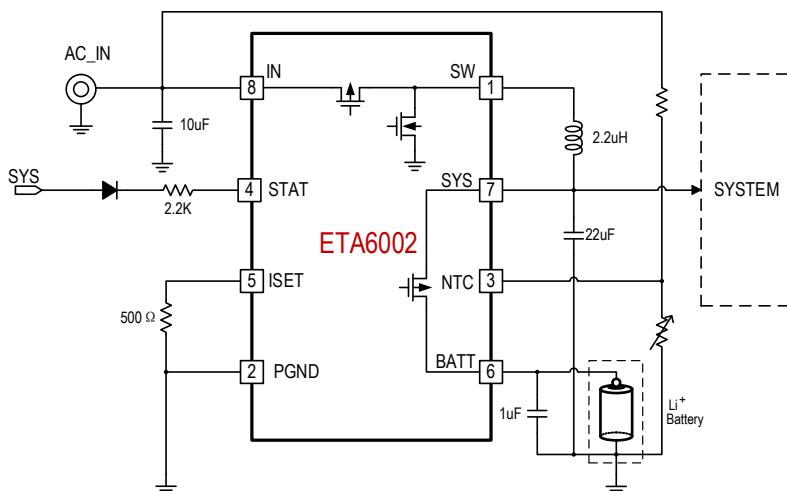
APPLICATIONS

- ◆ Tablet, MID
- ◆ Smart Phone
- ◆ Power Bank

ORDERING INFORMATION

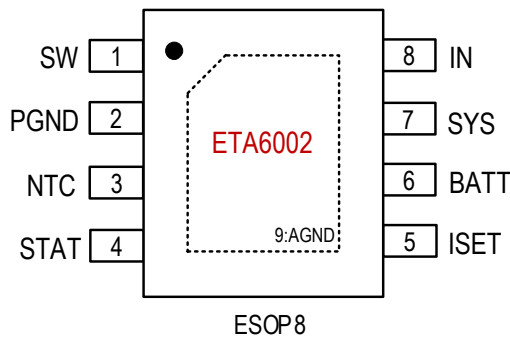
PART	PACKAGE	TOP MARK
ETA6002E8A	ESOP-8	ETA6002 YWWPL

TYPICAL APPLICATION



$$I_{BAT} = \frac{1000}{R_{ISET}}$$

PIN CONFIGURATION



ABSOLUTE MAXIMUM RATINGS

(Note: Exceeding these limits may damage the device. Exposure to absolute maximum rating conditions for long periods may affect device reliability.)

IN, BATT Voltage	-0.3V to 6V
All Other Pin Voltage.....	V _{IN} -0.3V to V _{IN} +0.3V
SW, SYS, BATT to ground current...	Internally limited
Operating Temperature Range.....	-40°C to 85°C
Storage Temperature Range.....	-55°C to 150°C
Thermal Resistance θ_{JA}	
ESOP8.....	50 °C/W

ELECTRICAL CHARACTERISTICS

(V_{IN} = 5V, unless otherwise specified. Typical values are at TA = 25°C.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
IN INPUT					
INPUT Range		4.4		5.5	V
INPUT UVLO	Rising, Hys=500mV		4.35		V
INPUT Operating Current	Switcher Enable, Switching		5		mA
	Switcher Enable, No Switching		70		µA
BATT to INPUT Leakage Current	Input Floating		0	5	µA
DC-DC and SYS OUTPUT					
VSYSMIN	I _{SYS} =1A, Default		3.6		V
VSYSMAX			4.5		V
Load Regulation			40		mV/A
Line Regulation	V _{IN} = 4.75 to 5.25V		0.04		%/V
Switching Frequency			3		MHz
Max Duty		100			%
HIGHSIDE MOS RDSON	I _{SW} =500mA		100		mΩ
LOWSIDE MOS RDSON	I _{SW} =500mA		60		mΩ
HIGHSIDE Current Limit			3.5		A
SYS UVLO	Falling, Hys=200mV		2.25		V
Thermal Shutdown	Rising, Hys=30°C		160		°C
POWER PATH Management					
BATT TO SYS RDSON			50		mΩ
BATTERY CHARGER					
Battery CV Voltage	I _{BAT} =0mA, Default	4.16	4.2	4.24	V
Charger Restart Threshold	From DONE to Fast Charge		-200		mV
Battery Pre-condition Voltage	V _{BAT} Rising Hys=180mV		2.9		V
Pre-condition Charge Current			100		mA
AC Fast Charge Current	R _{ISET1} =500Ω,		2		A

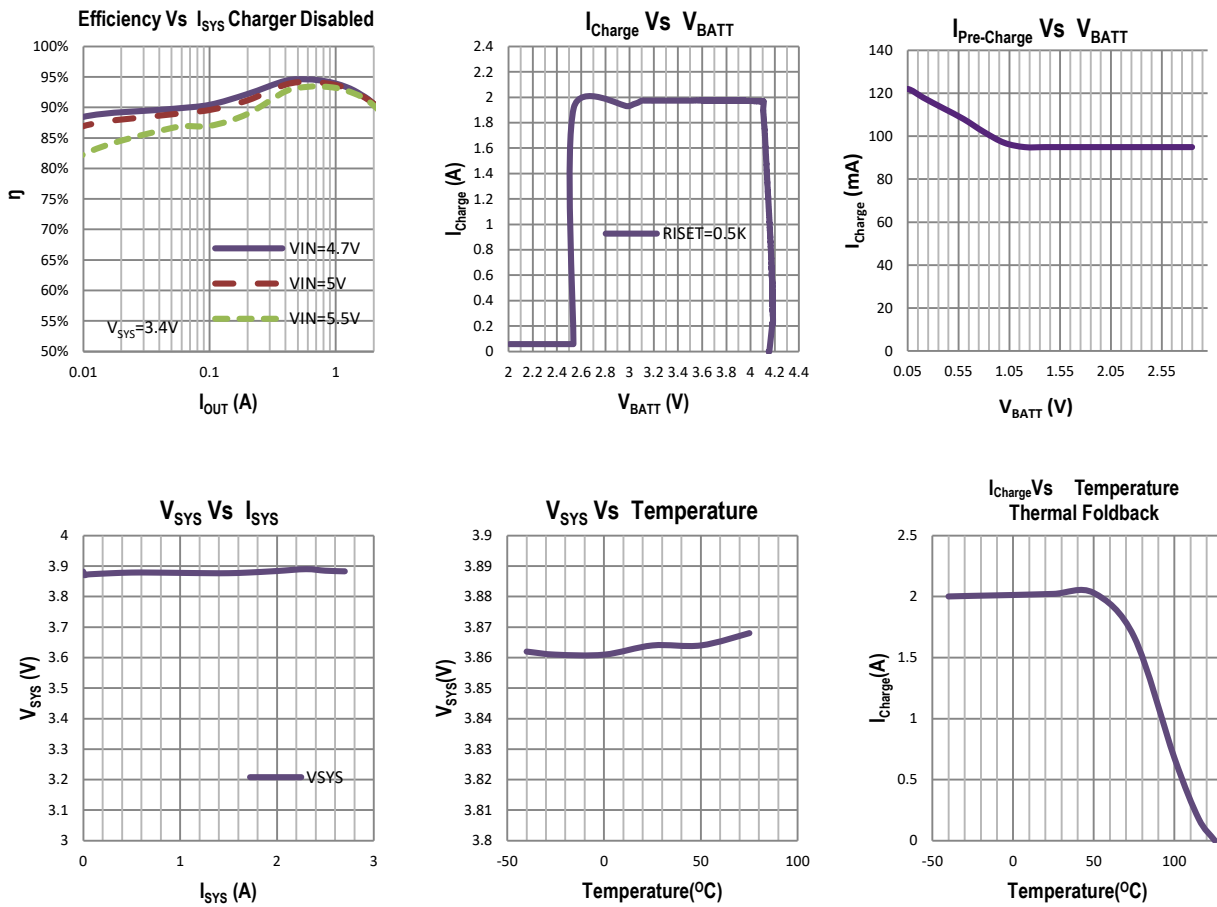
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
	nUSB_DET= V _{IN}				
	R _{ISET1} =2000Ω	0.45	0.5	0.55	A
Pre-Condition Timer			120		min
Fast-Charge Timer			960		min
THERMISTOR MONITOR					
NTC Threshold, Cold	Charger Suspended		76.5		%V _{IN}
NTC Threshold, Hot	Charger Suspended		35		%V _{IN}
NTC Threshold Hysteresis			1.5		%V _{IN}
NTC Disable Threshold			100		mV
NTC Input Leakage			0		μA
STATS					
STAT Output Low Voltage	I _{STATS} =10mA			0.2	V

PIN DESCRIPTION

PIN #	NAME	DESCRIPTION
1	SW	Switching node of the Switching Regulator. Connect a 1μH to 2.2μH inductor from this pin to SYS
2	PGND	Power Ground pin. Bypass with a 10μF capacitor to IN
3	NTC	Thermistor Input
4	STAT	Status pin for Charging status indications. An open drain device capable of driving 10mA current
5	ISET	Fast Charge Current set pin. Connecting a Resistor between ISET to GND This sets the fast charge current value $I_{BAT} = \frac{1000}{R_{ISET}}$
6	BATT	Battery pin. Connect a Battery to this pin
7	SYS	System Voltage pin. It is also the Switching regulator's output pin. Connect an inductor and capacitor to form the output filter
8	IN	Input pin. Can be connected to an AC adaptor or a USB charger output. Bypass with a 10μF capacitor each to PGND
9 (EP)	AGND	Exposed pad for analog ground connection. Must be connected to PGND on PCB

TYPICAL CHARACTERISTICS

(Typical values are at $T_A = 25^\circ\text{C}$ unless otherwise specified.)



FUNCTION DESCRIPTION

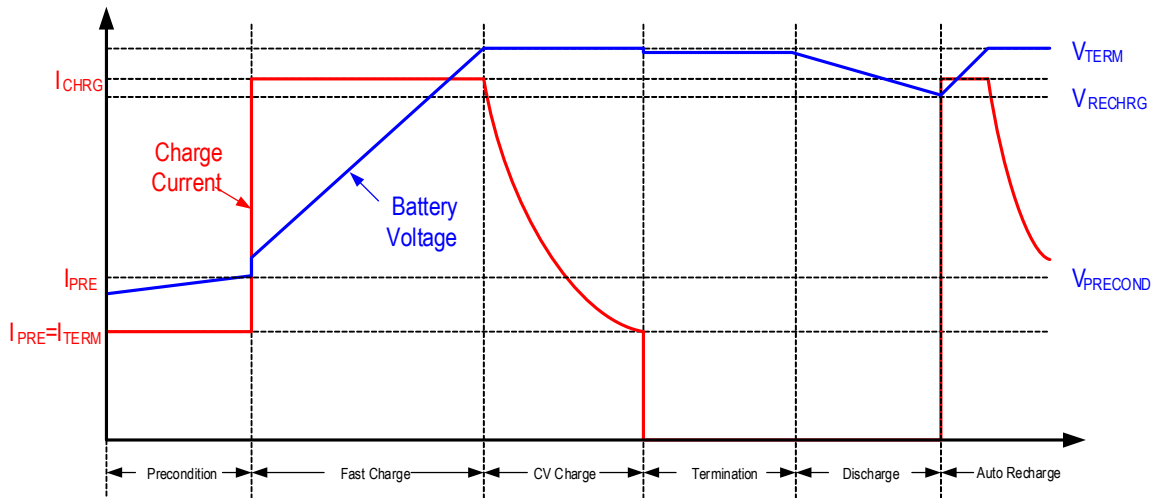
Status Indicators

STAT pin is a status pin for charging status indications. Please refer to Table 1.

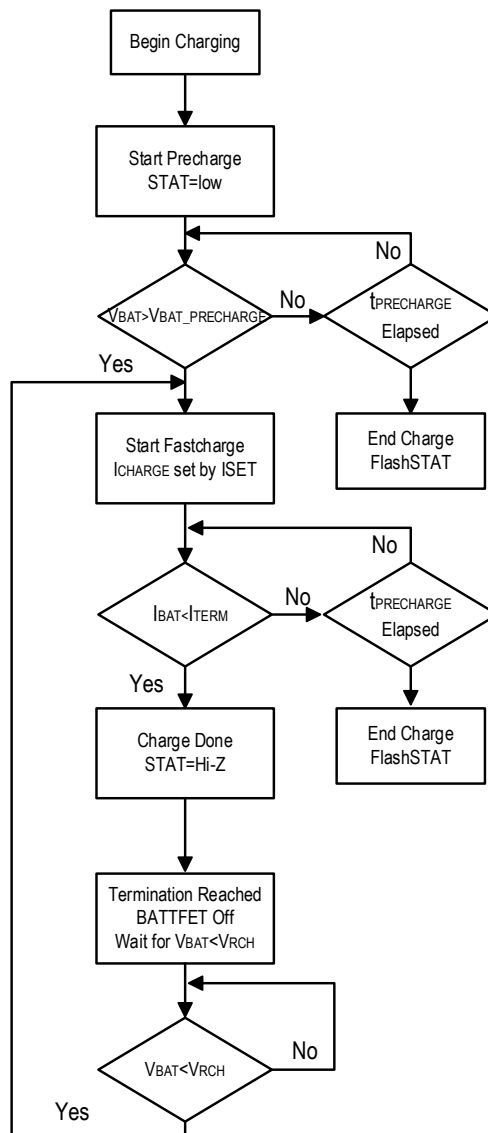
Table 1 STAT indicator

CHARGE STATE	STAT OUTPUT
Charging	Low
Charging done	High impedance
Safety timers expired	Flashing at 1 Hz
NTC protect	
No battery	

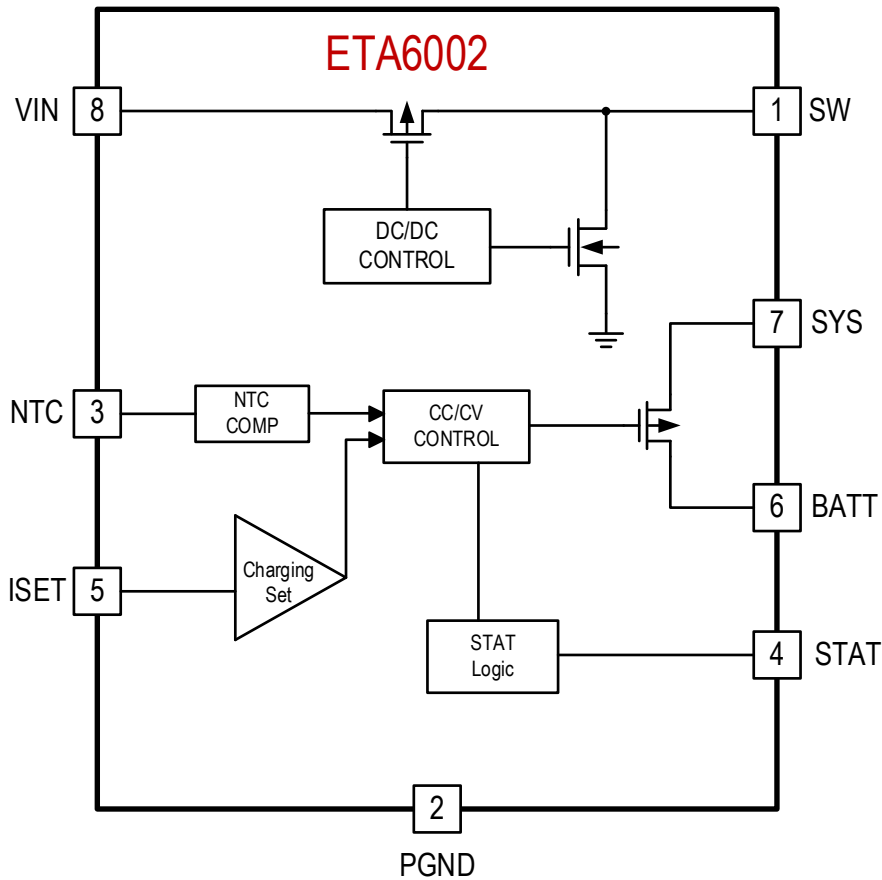
Battery Charge Profile



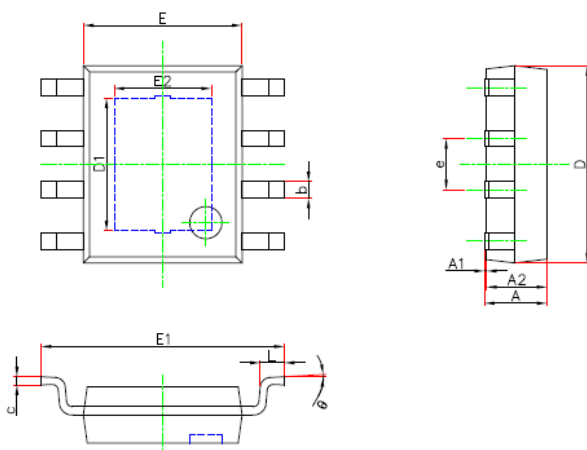
Charge Current Translator



FUNCTION BLOCK DIAGRAM



PACKAGE OUTLINE



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.300	1.700	0.051	0.067
A1	0.000	0.100	0.000	0.004
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.201
D1	3.202	3.402	0.126	0.134
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
E2	2.313	2.513	0.091	0.099
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°