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MAX33011E/12E/14E/15E +5V, 5Mbps CAN Transceiver with $\pm 65\text{V}$ Fault Protection, Fault Detection and Reporting, $\pm 25\text{V}$ CMR, and $\pm 45\text{kV}$ ESD Protection

General Description

The MAX33011E–MAX33012E, and MAX33014E–MAX33015E are a family of +5V CAN (Control Area Network) transceivers with integrated protection for industrial applications. These devices have extended $\pm 65\text{V}$ fault protection on the CAN bus for equipment where overvoltage protection is required. The CAN family also incorporates high $\pm 45\text{kV}$ ESD HBM protection and an input common mode range (CMR) of $\pm 25\text{V}$, exceeding the ISO11898 specification of -2V to $+7\text{V}$, and well suited for applications where ground planes from different systems are shifting relative to each other.

This CAN family incorporates a fault detection feature where it monitors the CANH and CANL line for faults like overcurrent, overvoltage, and transmission failure. When a fault is detected, the FAULT pin goes high, which triggers an external interrupt with the local CAN controller, and the driver and receiver lines are placed in recessive mode. The system clock then drives the TXD pin and a fault code is transmitted through the RXD line back to the controller. When the specific fault code is transmitted and the system is programmed to alert local service personnel, they are able to troubleshoot and debug the problem quicker, providing valuable diagnostics and decreased equipment downtime. In addition, the family features a variety of options to address common CAN application requirements; low-current standby mode, slow slew rate for improved EMI performance, silent-mode to disable the transmitter, low level translation to interface with low voltage controllers, and loopback mode for remote self diagnostics.

These devices are specified for data rates up to 5Mbps. The transceivers include a dominant timeout to prevent bus lockup caused by controller error or by a fault on the TXD input. When TXD remains in the dominant state (low) for longer than t_{DOM} , the driver is switched to the recessive state, releasing the bus.

The MAX33011E and MAX33012E are in standard 8-pin SOIC package and the MAX33014E and MAX33015E are in a 10-pin TDFN-EP package. The family of devices operate over the -40°C to $+125^{\circ}\text{C}$ temperature range.

Applications

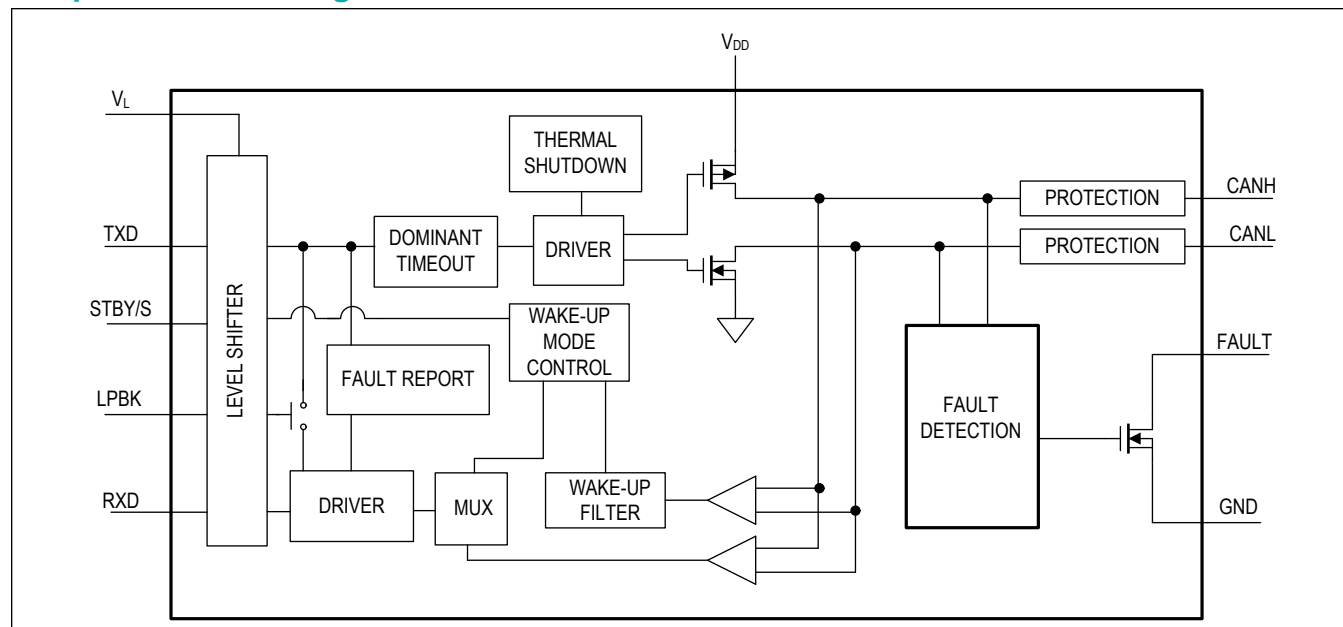
- Industrial Automation
- Building Automation
- Vending Machines
- Security Systems
- Heavy Equipment/Machinery

Benefits and Features

- Integrated Protection Increases Robustness
 - $\pm 65\text{V}$ Fault Tolerant CANH and CANL
 - $\pm 45\text{kV}$ ESD HBM (Human Body Model)
 - $\pm 30\text{kV}$ IEC Air-Gap and $\pm 12\text{kV}$ IEC Contact
 - $\pm 25\text{V}$ Extended Common Mode Input Range (CMR)
 - Transmitter Dominant Timeout Prevents Lockup
 - Short-Circuit Protection Limits Driver Current
 - Thermal Shutdown
- Family Provides Flexible Design Options
 - 1.62V to 5.5V Logic-Supply (V_L) Range (MAX33014E and MAX33015E)
 - Silent Mode S Disables Transmitter (MAX33011E and MAX33015E)
 - STBY Input For Low-Current, Slow Slew Rate, and Normal Mode (MAX33012E and MAX33014E)
 - LPBK Enables Remote Diagnostics (MAX33014E and MAX33015E)
- Fault Detection and Reporting
 - Detects Overcurrent, Overvoltage, and Transmission Failure
 - Error Code Reporting Through RXD
- Operating Temperature Range of -40°C to $+125^{\circ}\text{C}$

[Ordering Information](#) appears at end of data sheet.

Simplified Block Diagram



MAX33011E/12E/14E/15E

+5V, 5Mbps CAN Transceiver with $\pm 65V$ Fault Protection, Fault Detection and Reporting, $\pm 25V$ CMR, and $\pm 45kV$ ESD Protection

Absolute Maximum Ratings

V_{DD}	-0.3V to +6V	10 TDFN Single-Layer Board ($T_A = +70^\circ C$, derate 18.5mW/ $^\circ C$ above $+70^\circ C$.)	1481.5mW
CANH or CANL (Continuous).....	-65V to +65V	10 TDFN Multilayer Board ($T_A = +70^\circ C$, derate 24.4mW/ $^\circ C$ above $+70^\circ C$.)	1951.2mW
TXD, RXD, STBY, S, LPBK, FAULT	-0.3V to +6V	Operating Temperature Range	$-40^\circ C$ to $+125^\circ C$
V_L MAX33014E/15E	-0.3V to ($V_{DD} + 0.5V$)	Junction Temperature	$+150^\circ C$
Short-Circuit Duration	Continuous	Storage Temperature Range	$-60^\circ C$ to $+150^\circ C$
Continuous Power Dissipation		Soldering Temperature (reflow)	$+260^\circ C$
8 SO Single-Layer Board ($T_A = +70^\circ C$, derate 5.9mW/ $^\circ C$ above $+70^\circ C$.).....	470.6mW	Lead Temperature (soldering, 10sec).....	$+300^\circ C$
8 SO Multilayer Board ($T_A = +70^\circ C$, derate 7.6mW/ $^\circ C$ above $+70^\circ C$.).....	606.1mW		

Note 1: All voltages are referenced to GND, unless otherwise noted.

Note 2: Package thermal resistances were obtained using the method described in JEDEC specification JEDSD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package Information

8 SOIC

Package Code	S8+4
Outline Number	21-0041
Land Pattern Number	90-0096
Thermal Resistance, Single-Layer Board:	
Junction to Ambient (θ_{JA})	$170^\circ C/W$
Junction to Case (θ_{JC})	$40^\circ C/W$
Thermal Resistance, Four-Layer Board:	
Junction to Ambient (θ_{JA})	$132^\circ C/W$
Junction to Case (θ_{JC})	$38^\circ C/W$

10 TDFN

Package Code	T1034N+1
Outline Number	21-0268
Land Pattern Number	90-0247
Thermal Resistance, Single-Layer Board:	
Junction to Ambient (θ_{JA})	$54^\circ C/W$
Junction to Case (θ_{JC})	$9^\circ C/W$
Thermal Resistance, Four-Layer Board:	
Junction to Ambient (θ_{JA})	$41^\circ C/W$
Junction to Case (θ_{JC})	$9^\circ C/W$

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

Package thermal resistances were obtained using the method described in JEDEC specification JEDSD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to www.maximintegrated.com/thermal-tutorial.

MAX33011E/12E/14E/15E

+5V, 5Mbps CAN Transceiver with ±65V Fault Protection, Fault Detection and Reporting, ±25V CMR, and ±45kV ESD Protection

Electrical Characteristics

($V_{DD} = 4.5V$ to $5.5V$, $V_L = 1.62V$ to V_{DD} , $R_L = 60\Omega$, $C_L = 15pF$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise specified. Typical values are at $V_{DD} = 5V$, $V_L = 3.3V$ (MAX33014E-MAX33015E), and $T_A = +25^\circ C$, unless otherwise specified. (Note 1), Limits are 100% tested at $T_A = +25^\circ C$. Limits over the operating temperature range and relevant supply voltage range are guaranteed by design and characterization. Specifications marked "GBD" are guaranteed by design and not production tested.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Power							
Supply Voltage	V _{DD}			4.5		5.5	V
Logic Supply Voltage	V _L	MAX33014E–MAX33015E		1.62		V _{DD}	V
Dominant Supply Current	I _{DD_DOM}	TXD = 0V	No load		5	8	mA
			R _L = 60Ω		50	70	
Recessive Supply Current	I _{DD_REC}	V _{DD} = 5V, TXD = V _L	No load		4		mA
			CANH shorted to CANL		4		
Standby Supply Current	I _{STBY}	STBY = V _L , no external pullup resistor on Fault pin			60		μA
Silent Mode Supply Current	I _S	S = V _L			2.5		mA
Logic Supply Current	I _L	RXD = open	V _L = 5V		60		μA
			V _L = 3.3V		40		
			V _L = 1.8V		22		
UVLO Threshold Rising	V _{UVLO_R}	V _{DD} rising				4.25	V
UVLO Threshold Falling	V _{UVLO_F}	V _{DD} falling		3.45			V
Protection							
ESD CANH and CANL		HBM	JEDEC JS-001, Note 3		±45		kV
		Air Gap	ISO10605, IEC 61000-4-2		±30		
		Contact	ISO10605, IEC 61000-4-2		±12		
ESD Other pins	ESD _{ALL}	HBM	JEDEC JS-001, Note 3		±4		kV
Fault Protection Range	V _{FP}	CANH or CANL to GND		-65		+65	V
Thermal Shutdown	T _{SHDN}				+160		°C
Thermal Shutdown Hysteresis	T _{HYST}				+20		°C
Logic Interface (RXD, TXD, STBY, S, LPBK)							
Input High Voltage	V _{IH}			0.7 x V _L			V
Input Low Voltage	V _{IL}	2.25V ≤ V _L ≤ 5.5V		0.8			V
		1.62V ≤ V _L ≤ 2.25V		0.6			
TXD Input Pullup Resistance	R _{PU_TXD}			100		250	kΩ
STBY, S Input Pullup Resistance/LPBK Pulldown Resistance	R _{PU_S}			100		250	kΩ

Electrical Characteristics (continued)

($V_{DD} = 4.5V$ to $5.5V$, $V_L = 1.62V$ to V_{DD} , $R_L = 60\Omega$, $C_L = 15pF$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise specified. Typical values are at $V_{DD} = 5V$, $V_L = 3.3V$ (MAX33014E-MAX33015E), and $T_A = +25^\circ C$, unless otherwise specified. (Note 1), Limits are 100% tested at $T_A = +25^\circ C$. Limits over the operating temperature range and relevant supply voltage range are guaranteed by design and characterization. Specifications marked "GBD" are guaranteed by design and not production tested.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output High Voltage (RXD)	V _{OH}	Sourcing 4mA		V _L - 0.4			V
Output Low Voltage (RXD)	V _{OL}	Sinking 4mA				0.4	V
CAN Bus Driver							
Bus Output Voltage (Dominant)	V _{O_DOM}	t < t _{DOM} , TXD = 0V, R _L = 60Ω	CANH	2.75		4.5	V
			CANL	0.5		2.25	
Bus Output Voltage (Recessive)	V _{O_REC}	TXD = V _L , no load		2		3	V
Bus Output Differential Voltage (Dominant)	V _{OD_DOM}	TXD = 0V, R _L = 60Ω	-5V ≤ V _{CM} ≤ 10V, R _{CM} = 156Ω, Figure 1	1.5		3	V
		TXD = 0V, R _L = 60Ω	R _{CM} = open	1.5		3	
Bus Output Differential Voltage (Recessive)	V _{OD_REC}	TXD = V _L	R _L = 60Ω	-120		+12	mV
			No load	-500		+50	
Bus Output Voltage Standby	V _{O_STBY}	TXD = STBY = V _L	No load	70		160	mV
High-Side Short-Circuit Current	I _{SC_CANH}	TXD = CANH = 0V			175	250	mA
Low-Side Short-Circuit Current	I _{SC_CANL}	TXD = 0V, CANL = V _{DD}			175	250	mA
Receiver							
Common Mode Input Range	V _{CM}	CANH or CANL to GND, RXD output valid		-25		+25	V
Common Mode Input Range Standby Mode	V _{CM_STBY}	CANH or CANL to GND, RXD output valid		-12		+12	V
Input Differential Threshold Voltage (Dominant)	V _{ID_DOM}	-25V ≤ V _{CM} ≤ +25V, TXD = V _L				0.9	V
Input Differential Threshold Voltage (Recessive)	V _{ID_REC}	-25V ≤ V _{CM} ≤ +25V, TXD = V _L		0.5			V
Standby Input Differential Threshold Voltage (Dominant)	V _{ID_SDOM}	-12V ≤ V _{CM} ≤ +12V, TXD = V _L				1.15	V
Standby Input Differential Threshold Voltage (Recessive)	V _{ID_SREC}	-12V ≤ V _{CM} ≤ +12V, TXD = V _L		0.45			V
Input Differential Hysteresis	V _{ID_HYS}	-25V ≤ V _{CM} ≤ +25V			100		mV

Electrical Characteristics (continued)

($V_{DD} = 4.5V$ to $5.5V$, $V_L = 1.62V$ to V_{DD} , $R_L = 60\Omega$, $C_L = 15pF$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise specified. Typical values are at $V_{DD} = 5V$, $V_L = 3.3V$ (MAX33014E-MAX33015E), and $T_A = +25^\circ C$, unless otherwise specified. (Note 1), Limits are 100% tested at $T_A = +25^\circ C$. Limits over the operating temperature range and relevant supply voltage range are guaranteed by design and characterization. Specifications marked "GBD" are guaranteed by design and not production tested.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Input Resistance	R_{IN}	$TXD = V_L$		10		50	$k\Omega$
Differential Input Resistance	R_{IN_DIFF}	$TXD = V_L$		20		100	$k\Omega$
Input Capacitance	C_{IN}	$TXD = V_L$ (Note 2)			22	39	pF
Differential Input Capacitance	C_{IN_DIFF}	$TXD = V_L$ (Note 2)			12	22	pF
Input Leakage Current	I_{LKG}	$V_{DD} = V_L = 0V$	$CANH = CANL = V_{DD}$		150	280	μA
Switching							
Data Rate		$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			5		Mbps
Driver Rise Time	t_R	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			10		ns
Driver Fall Time	t_F	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			14		ns
Slow Slew Driver Rise Time	t_{SSR}	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			160		ns
Slow Slew Driver Fall Time	t_{SSF}	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			130		ns
TXD to RXD Loop Delay	t_{LOOP}	$R_L = 60\Omega$, Dominant to Recessive and Recessive to Dominant, Figure 2			60	100	ns
TXD Propagation Delay (Recessive to Dominant)	t_{ONTXD}	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			30	50	ns
TXD Propagation Delay (Dominant to Recessive)	t_{OFFTXD}	$R_L = 60\Omega$, $C_{LD} = 100pF$, R_{CM} is open, Figure 1			25	50	ns
RXD Propagation Delay (Recessive to Dominant)	t_{ONRXD}	$C_L = 15pF$, Figure 3			25	70	ns
RXD Propagation Delay (Dominant to Recessive)	t_{OFFRXD}	$C_L = 15pF$, Figure 3			30	70	ns
TXD-Dominant TimeOut	t_{DOM}	Figure 4		1.3		4.3	ms
Wake Up Time	t_{WAKE}	Figure 5			2.2		μs
Fault Interrupt On Delay (Overcurrent, Overvoltage, Transmission Failure)	t_{FAULT_LH}	100k Ω pullup resistor to V_{DD} , 2.5MHz signal on TXD, Figure 6	Overcurrent and overvoltage		4		μs
			Transmission Failure		8		
Loopback Delay from Transmitter Input to Receiver Output	t_{LPBK}	MAX33014E/MAX33015E			10		ns
Standby to Normal Mode Delay	t_{STBY_NORM}				20		μs

MAX33011E/12E/14E/15E

+5V, 5Mbps CAN Transceiver with ±65V Fault Protection, Fault Detection and Reporting, ±25V CMR, and ±45kV ESD Protection

Electrical Characteristics (continued)

(V_{DD} = 4.5V to 5.5V, V_L = 1.62V to V_{DD}, R_L = 60Ω, C_L = 15pF, T_A = T_{MIN} to T_{MAX}, unless otherwise specified. Typical values are at V_{DD} = 5V, V_L = 3.3V (MAX33014E-MAX33015E), and T_A = +25°C, unless otherwise specified. (Note 1), Limits are 100% tested at T_A = +25°C. Limits over the operating temperature range and relevant supply voltage range are guaranteed by design and characterization. Specifications marked "GBD" are guaranteed by design and not production tested.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Normal to Standby Mode Delay	t _{NORM_STBY}			40		μs
Standby Propagation Delay	t _{PLH_STBY}	C _L = 15pF, Figure 5		300		ns

- Note 1:** All units are 100% production tested at T_A = +25°C. Specifications over temperature are guaranteed by design.
- Note 2:** Not production tested. Guaranteed at T_A = 25°C.
- Note 3:** Tested at the limits of test equipment

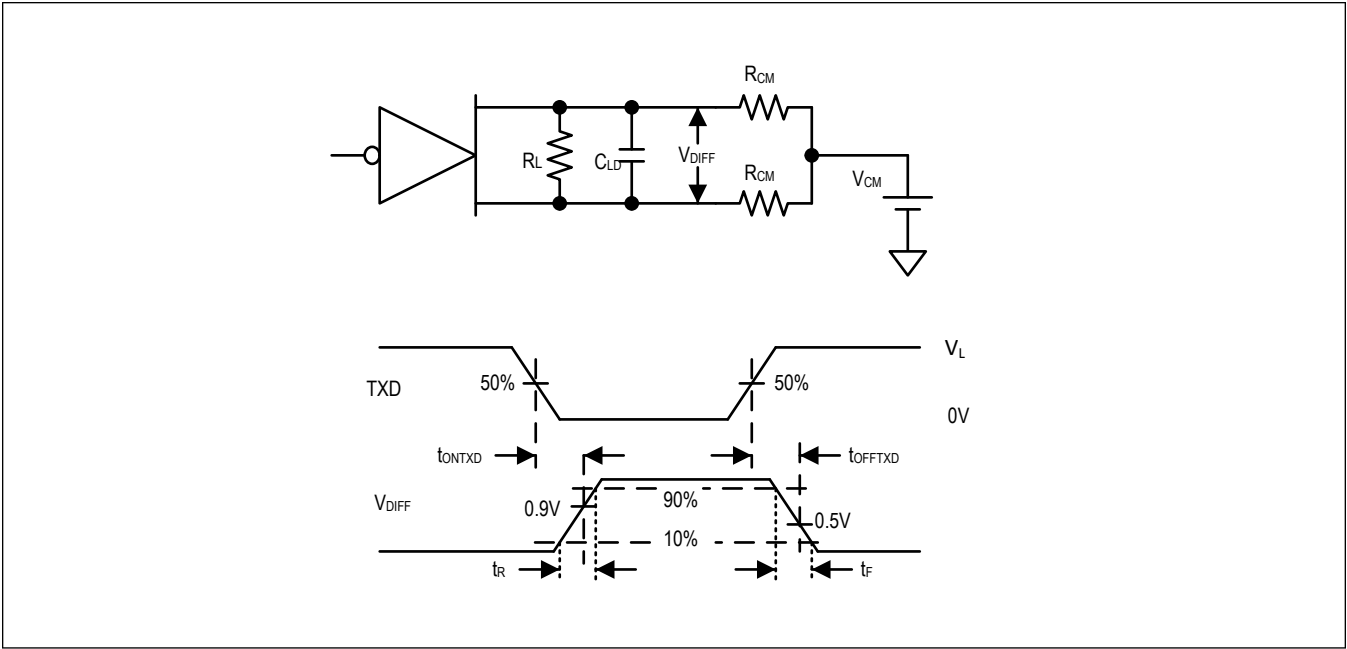


Figure 1. Transmitter Test Circuit and Timing Diagram

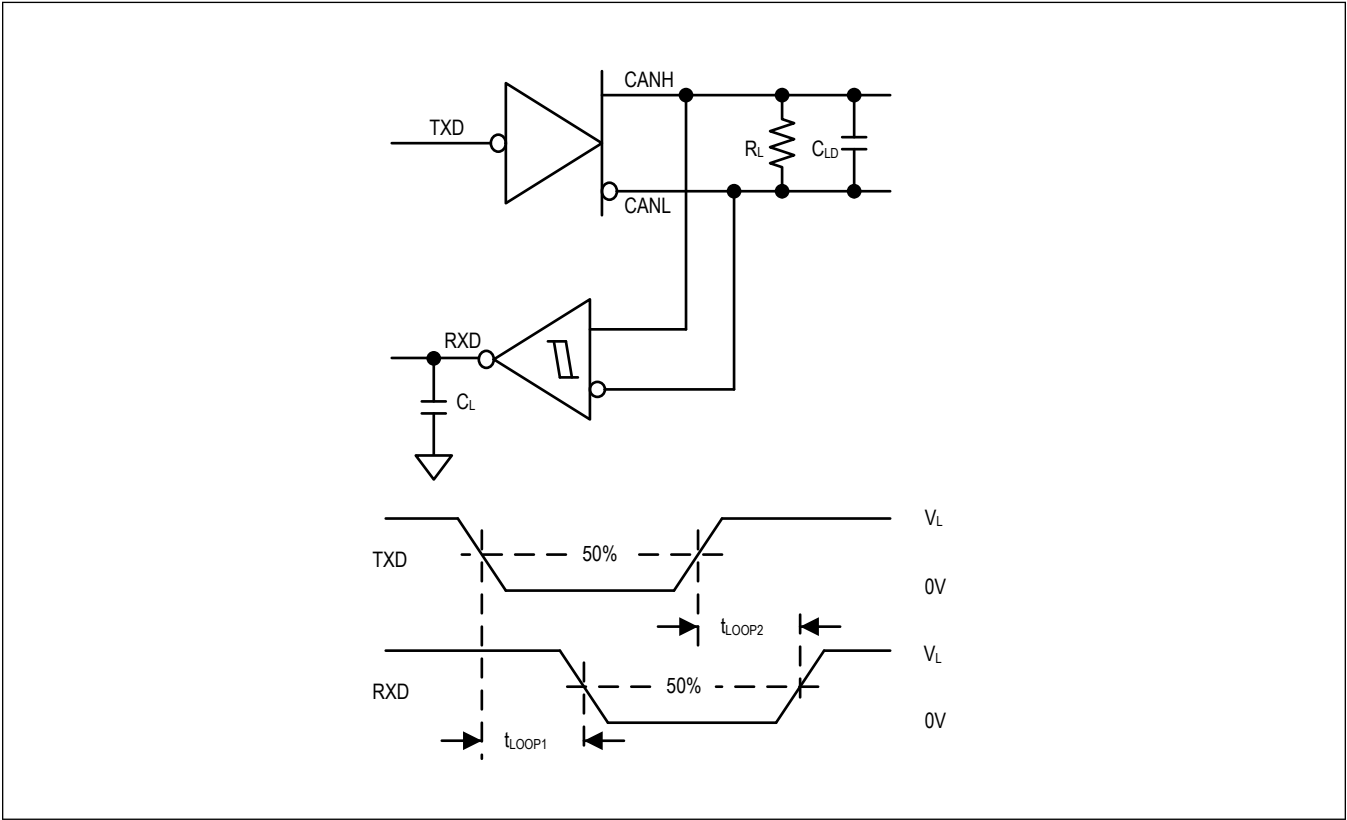


Figure 2. TXD to RXD Loop Delay

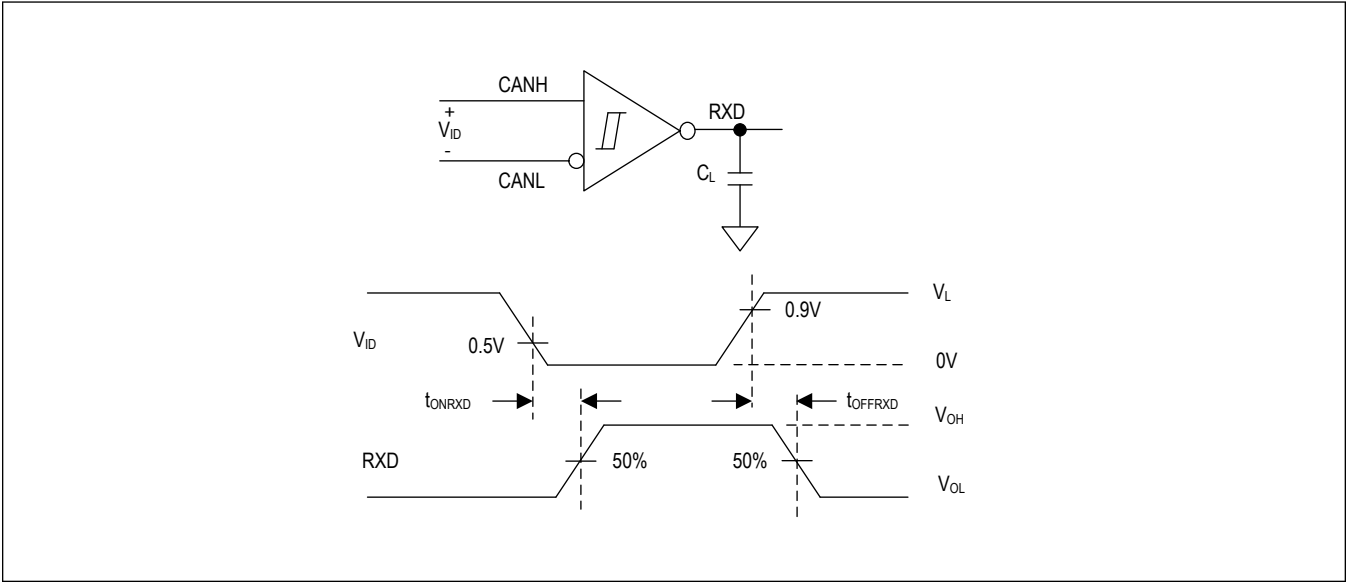


Figure 3. RXD Timing Diagram

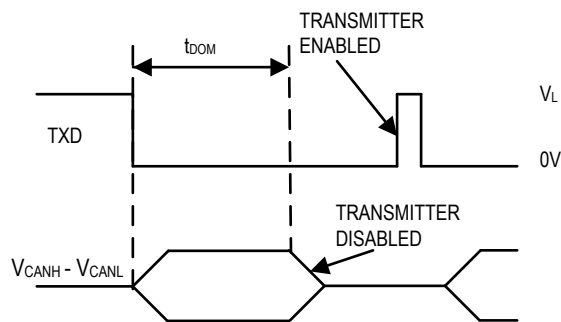


Figure 4. Transmitting Dominant Timeout Timing Diagram

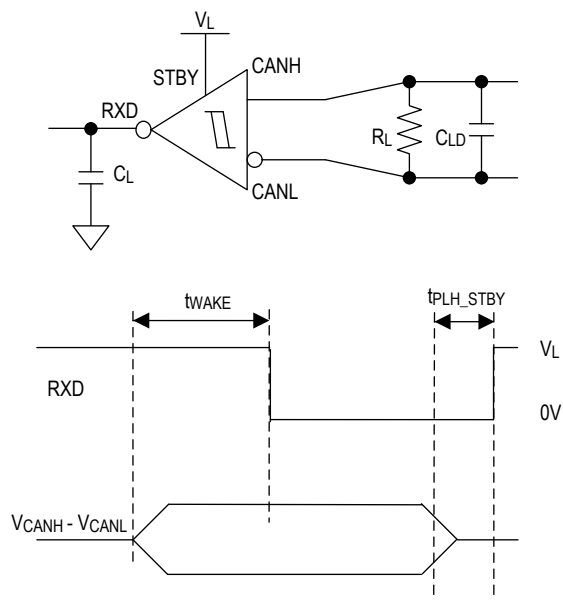


Figure 5. Standby Receiver Propagation Delay

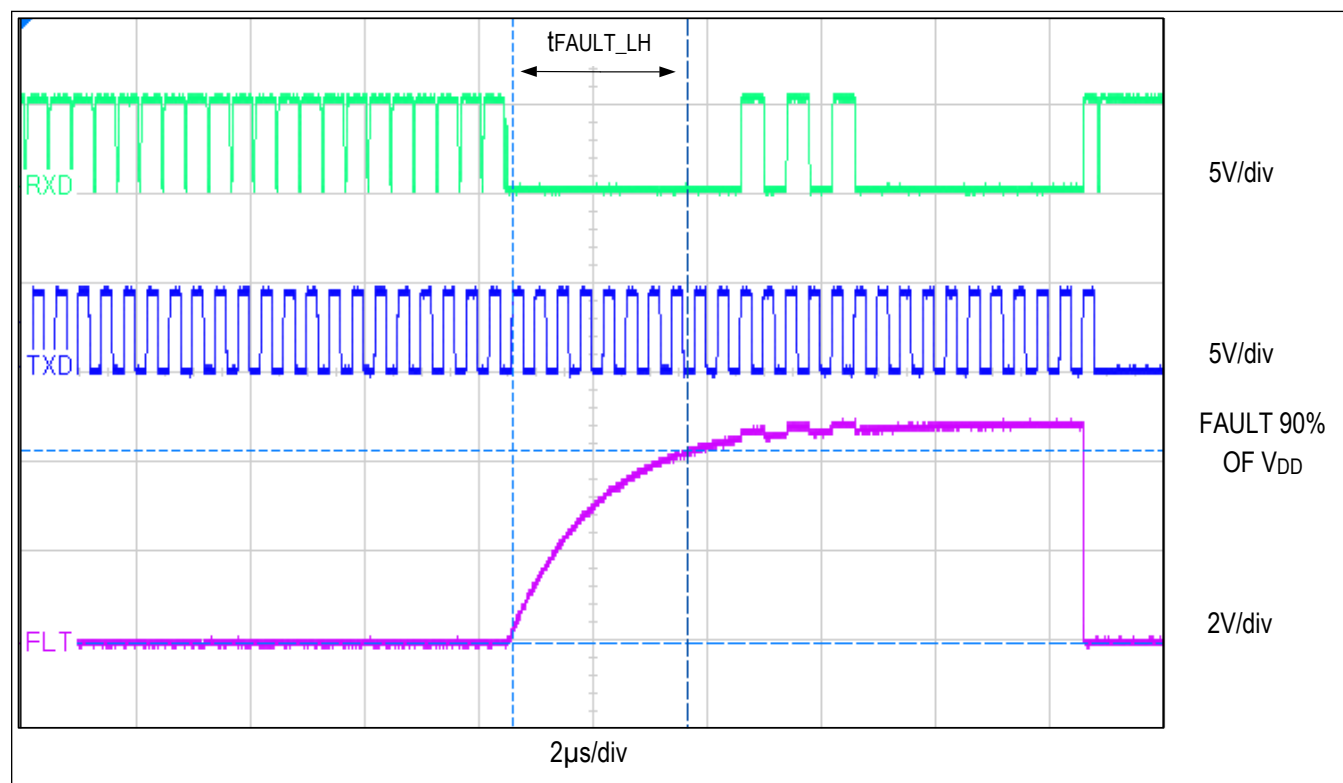


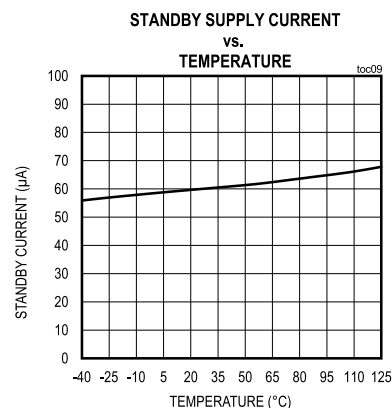
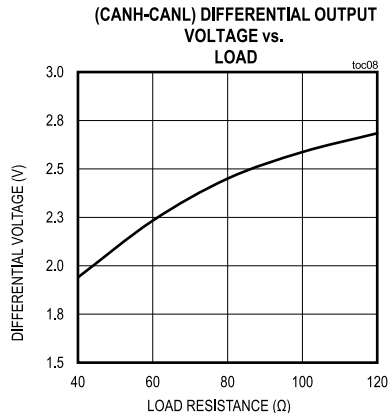
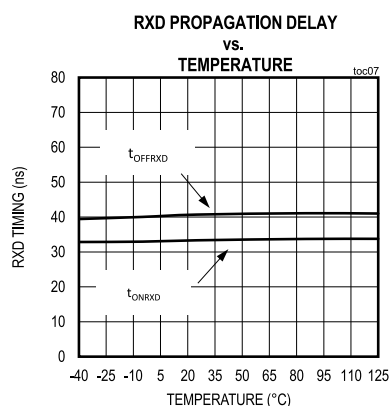
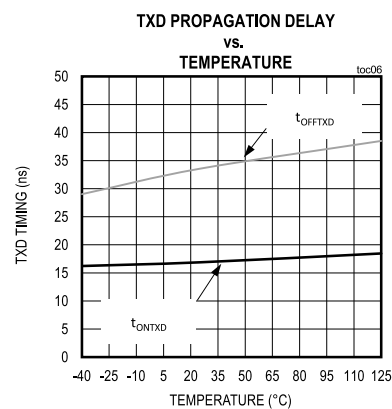
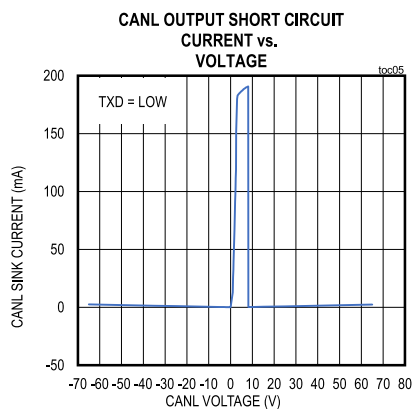
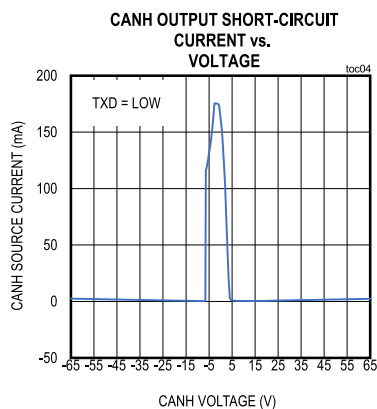
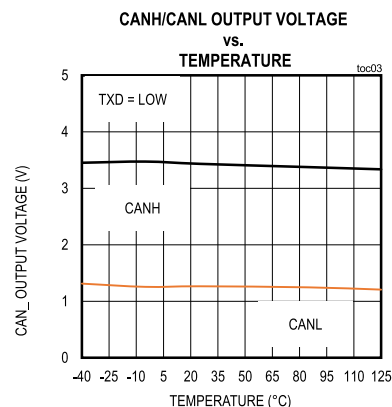
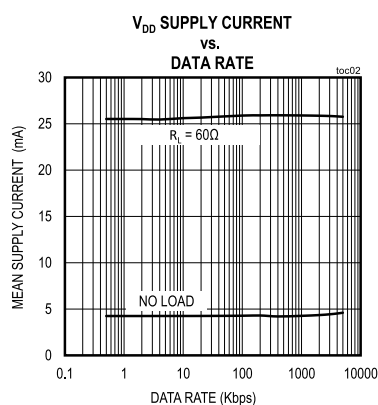
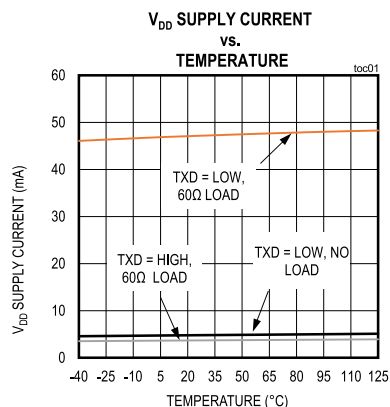
Figure 6. FAULT Propagation Delay From Low to High

MAX33011E/12E/14E/15E

+5V, 5Mbps CAN Transceiver with $\pm 65\text{V}$ Fault Protection, Fault Detection and Reporting, $\pm 25\text{V}$ CMR, and $\pm 45\text{kV}$ ESD Protection

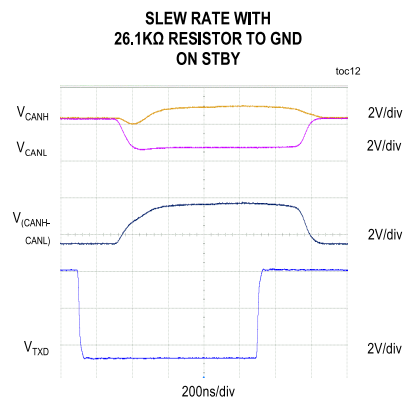
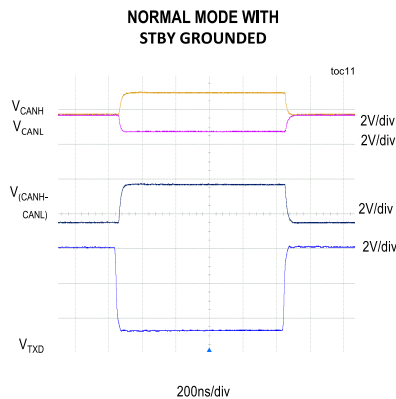
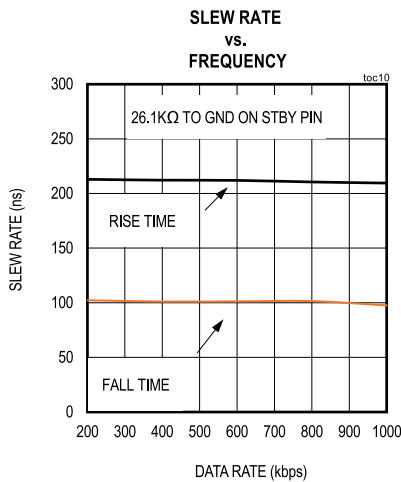
Typical Operating Characteristics

($V_{DD} = 5\text{V}$, $V_L = 3.3\text{V}$, $R_L = 60\Omega$, $C_L = 15\text{pF}$, $T_A = +25^\circ\text{C}$, unless otherwise noted.)



Typical Operating Characteristics (continued)

($V_{DD} = 5\text{V}$, $V_L = 3.3\text{V}$, $R_L = 60\Omega$, $C_L = 15\text{pF}$, $T_A = +25^\circ\text{C}$, unless otherwise noted.)

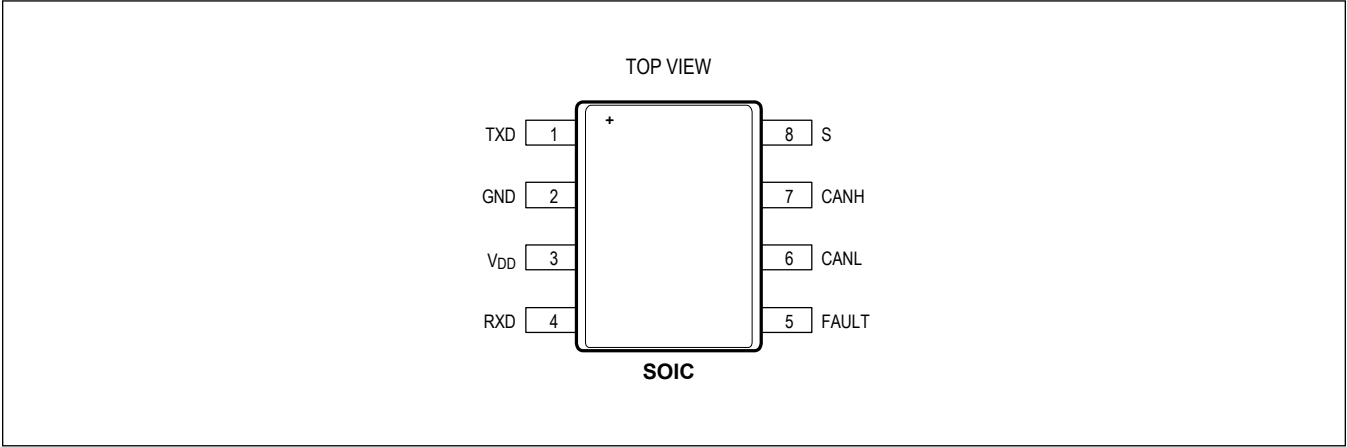


MAX33011E/12E/14E/15E

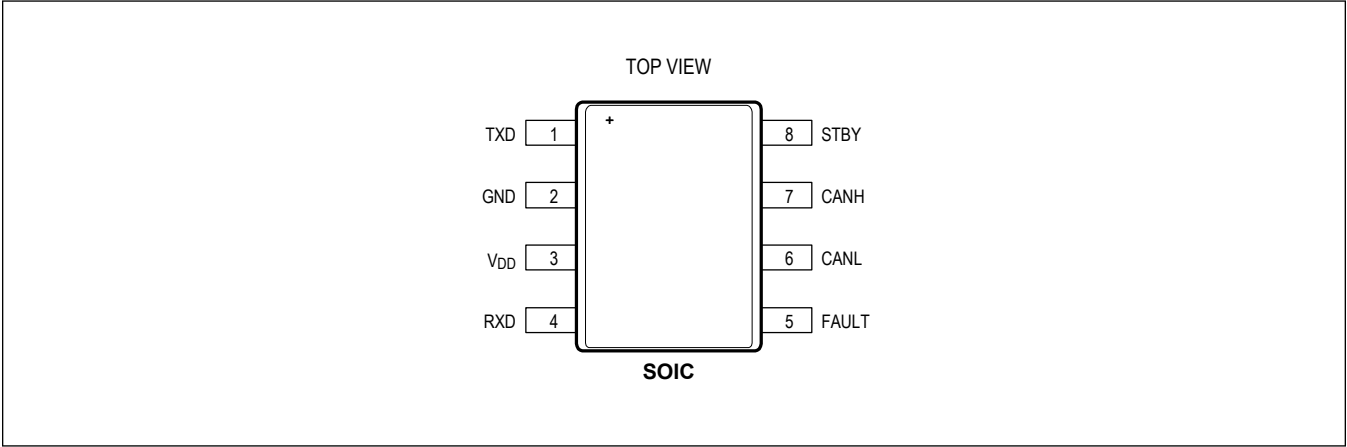
+5V, 5Mbps CAN Transceiver with ±65V Fault Protection, Fault Detection and Reporting, ±25V CMR, and ±45kV ESD Protection

Pin Configurations

MAX33011E



MAX33012E



+5V, 5Mbps CAN Transceiver with $\pm 65\text{V}$ Fault Protection, Fault Detection and Reporting, $\pm 25\text{V}$ CMR, and $\pm 45\text{kV}$ ESD Protection

TOP VIEW

Pin	Signal
1	TXD
2	LPBK
3	GND
4	VDD
5	RXD
6	FAULT
7	VL
8	CANL
9	CANH
10	STBY

TDFN-EP
3mm x 4mm

TOP VIEW

The diagram shows the top view of the TDFN-EP package. It is a rectangular package with a central square area defined by a dashed line. The package is oriented with pins 1 through 5 on the left side and pins 6 through 10 on the right side. A '+' symbol is located in the top-left corner of the package, indicating the orientation. The pins are labeled as follows:

Pin	Signal
1	TXD
2	LPBK
3	GND
4	VDD
5	RXD
6	FAULT
7	VL
8	CANL
9	CANH
10	S

TDFN-EP
3mm x 4mm

PIN				NAME	FUNCTION
MAX33011E	MAX33012E	MAX33014E	MAX33015E		
1	1	1	1	TXD	Transmit Data Input. TXD is a CMOS/TTL compatible input from a CAN controller with an internal pullup to V _{DD} .
—	—	2	2	LPBK	Loopback Mode. When connected to logic-high, the transmitter and receiver are placed in a high-impedance state. Internally, the TXD line drives directly to the RXD line for loopback that can be used for self-diagnostic of the transceiver without disturbing the bus. When loopback mode is not used, connect to GND or leave floating.
2	2	3	3	GND	Ground
3	3	4	4	VDD	Supply Voltage. Bypass V _{DD} to GND with a 0.1μF capacitor.

Pin Description (continued)

PIN				NAME	FUNCTION
MAX33011E	MAX33012E	MAX33014E	MAX33015E		
4	4	5	5	RXD	Receive Data Output. RXD is a CMOS/TTL compatible output from the physical bus lines CANH and CANL.
5	5	6	6	FAULT	Fault Detected Indication. Stays low when fault is not detected. Release to high if an overcurrent, overvoltage, or transmission failure is detected.
—	—	7	7	VL	Logic Supply Input. V_L is the logic supply voltage for the input/output between the CAN transceiver and controller. V_L allows full compatibility from +1.62V to +5.5V logic on all digital lines. Bypass to GND with a 0.1 μ F capacitor. Connect V_L to V_{DD} for 5V logic compatibility.
6	6	8	8	CANL	CAN Bus Line Low
7	7	9	9	CANH	CAN Bus Line High
8	—	—	10	S	Silent Mode Input. Drive S low to enable TXD and to operate in normal mode. Drive S high to disable the transmitter.
—	8	10	—	STBY	Standby Mode Input. A logic-high on STBY pin selects the standby mode. In standby mode, the transmitter is disabled, and the receiver is in low-power mode. A logic-low on STBY pin puts the transceiver in normal operating mode. A 26.1k Ω external resistor can be used to connect the STBY pin to ground for slow slew mode.

Detailed Description

The MAX33011E–MAX33012E and MAX33014E–MAX33015E are a family of fault protected CAN transceivers with fault detection and fault reporting. They are designed for applications where expeditious troubleshooting is important to increase the up time of important control systems, addressing common faults like overcurrent, overvoltage, and transmission failure. These devices are ideal for harsh industrial applications with a number of integrated robust protection feature set that improve the reliability of end equipment. These devices provide a link between the CAN protocol controller and the physical wires of the bus lines in a control area network (CAN). They can be used for DeviceNet™, CAN Kingdom, and CANOpen™ applications as well.

All CAN transceivers in the family are fault protected up to $\pm 65V$, making it suitable for applications where overvoltage protection is required. These devices are rated up to a high $\pm 45kV$ ESD of HBM (human body model), suitable for protection during the manufacturing process, and even in the field where there is human interface for installation and maintenance. In addition, a common mode voltage range of $\pm 25V$ enables communication in noisy environments where there are ground plane differences between different systems due to close proximity of heavy equipment machinery or operation from different transformers. Dominant timeout prevents the bus from being blocked by a hung-up microcontroller, and the outputs CANH and CANL are short-circuit current-limited and protected against excessive power dissipation by thermal shutdown circuitry that places the driver outputs in a high-impedance state.

The MAX33011E–MAX33012E and MAX33014E–MAX33015E can operate up to 5Mbps. The MAX33011E and MAX33012E have the option to slow the slew rate to $8V/\mu s$ to minimize EMI, enabling the use of unshielded-twisted or parallel cable. The MAX33011E and MAX33012E are packaged in an industry standard 8-pin SO, while the MAX33014E and MAX33015E are packaged in a 10-pin TDFN. The family of robust CAN transceivers have an operating temperature from $-40^{\circ}C$ to $+125^{\circ}C$.

$\pm 65V$ Fault Protection

These devices feature $\pm 65V$ of fault protection. The CANH and CANL data lines are capable of withstanding a short from $-65V$ to $+65V$. This extended overvoltage range makes it suitable for applications where accidental shorts to power supply lines are possible due to human intervention.

Transmitter

The transmitter converts a single-ended input signal (TXD) from the local CAN controller to differential outputs for the bus lines CANH and CANL. The truth table for the transmitter and receiver is provided in [Table 1](#).

Table 1. Transmitter and Receiver Truth Table (When Not Connected to the Bus)

STBY	TXD	TXD LOW TIME	CANH	CANL	BUS STATE	RXD
LOW	LOW	$< t_{DOM}$	HIGH	LOW	DOMINANT	LOW
LOW	LOW	$> t_{DOM}$	$V_{DD}/2$	$V_{DD}/2$	RECESSIVE	HIGH
LOW	HIGH	X	$V_{DD}/2$	$V_{DD}/2$	RECESSIVE	HIGH

X = Don't care

Transmitter Output Protection

The family of CAN transceivers protect the transmitter output stage against a short-circuit to a positive or negative voltage by limiting the driver current. See the CANH and CANL short-circuit current TOC graphs. Thermal shutdown further protects the devices from excessive temperatures that may result from a short. The transmitter returns to normal operation once the short is removed.

Transmitter-Dominant Timeout

The devices feature a transmitter dominant timeout (t_{DOM}) that prevents erroneous CAN controllers from clamping the bus to a dominant level by maintaining a continuous low TXD signal. When TXD remains in the dominant state (low) for greater than t_{DOM} , the transmitter is disabled, releasing the bus to a recessive state (Figure 4). After a dominant timeout fault, the transmitter is re-enabled when receiving a rising edge at TXD. The transmitter dominant timeout limits the minimum possible data rate to 9Kbps for standard CAN protocol.

Receiver

The receiver reads the differential input from the bus line CANH and CANL and transfers this data as a single-ended output RXD to the CAN controller. It consists of a comparator that senses the difference $V_{DIFF} = (CANH - CANL)$, with respect to an internal threshold of 0.7V. If $V_{DIFF} > 0.9V$, a logic-low is present on RXD. If $V_{DIFF} < 0.5V$, a logic-high is present. The CANH and CANL common-mode range is $\pm 25V$ in normal mode and $\pm 12V$ in standby mode. RXD is a logic-high when CANH and CANL are shorted or terminated and undriven.

Fault Detection and Reporting

This family of devices have fault detection for overcurrent, overvoltage, and transmission failure in normal mode operation. The detection of faults and reporting them out to the local CAN controller provide additional information that benefits the troubleshooting of a given problem in a CAN bus system, reducing down time, improving equipment efficiencies, and keeping service costs down.

To enable fault detection upon power-up, 100 low-to-high transitions need to pass through TXD, which is typically 1 or 2 CAN frame messages depending on data payload size (classic or extended format) and which protocol is used. Fault detection is not enabled in standby and silent mode. After the 100 low-to-high transitions on TXD, if a fault is detected, then another 16 low-to-high transitions on TXD are required to shift out the fault code shown in Table 2. In addition, 10 more pulses are needed to clear the fault.

Table 2. Fault Detection and Reporting

FAULT	CONDITION (FAULT DETECTION ENABLED)	FAULT CODE	FAULT PRIORITY	POSSIBLE CAUSE
Overcurrent	$>85mA$	101010	1	- CANH shorted to CANL - CANH connected to GND and CANL connected to V_{DD}
Overvoltage	$CANH > +29V$ or $CANL < -29V$	101100	2	CMR fault
Transmission Failure	RXD unchanged for 10 consecutive pulses, recommended minimum frequency = 200kHz	110010	3	- Open load (both termination resistors missing) on CANH and CANL - Exceeds driver's common mode range - CANH and/or CANL connected to a fixed voltage source

Overvoltage Detection

Over voltage detection is triggered when CANH is above approximately +29V or CANL is below approximately -29V. This indicates that the CAN bus has likely violated the CMR range or that a short fault on CANH and/or CANL has occurred

and is beyond the $\pm 29\text{V}$ threshold. Once overvoltage detection is triggered, the fault pin transitions from low to high and the fault code is clocked out of RXD through TXD.

Overcurrent Detection

Over current detection is triggered when there is a high current path from V_{DD} to GND through a short from CANH to CANL. In addition, shorts far away from the CAN node may not be detected due to high cable impedance. See [Figure 7](#) for overcurrent detection maximum operating frequency versus cable length as a reference. A Cat5e copper clad aluminum cable is used. Maximum frequency will vary with the type of cable.

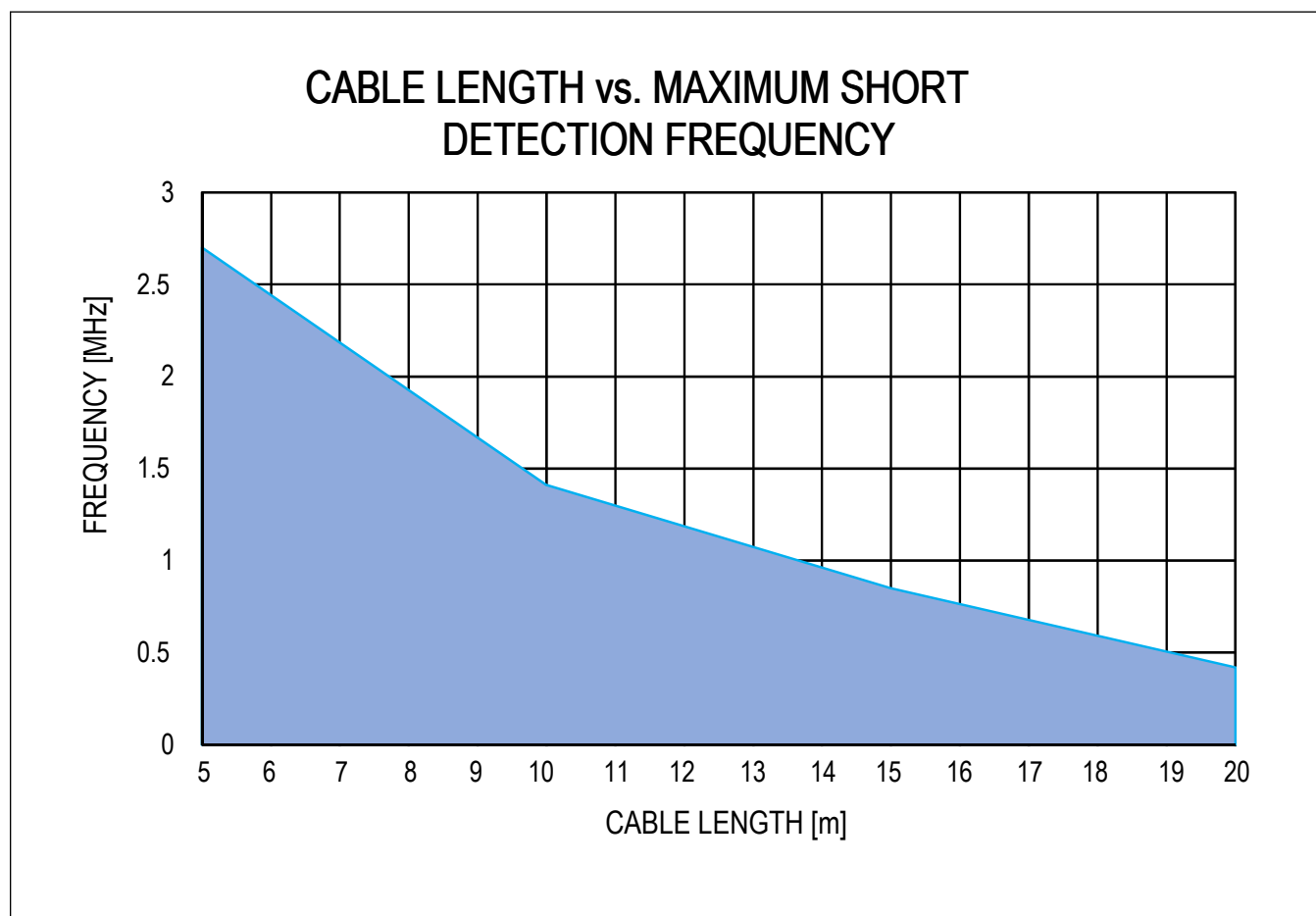


Figure 7. Overcurrent Detection Operating Frequency Versus Cable Length

Transmission Failure Detection

Transmission failure detection is triggered when the signal on RXD does not match TXD for 10 consecutive cycles after fault detection is enabled. This can occur when both termination resistors are missing. Other scenarios include, but not limited to, shorting CANH to GND or CANL to V_{DD} resulting in the differential signal not meeting the receiver's V_{IH} and V_{IL} specification.

Fault Reporting

When a fault occurs and FAULT pin goes high, the CANH and CANL lines are placed in high impedance, and a 6-bit fault code is stored in an internal register. 16 low-to-high transitions need to go through TXD to shift the fault code out through RXD. The circuit will look for any faults and determine the fault priority—overcurrent (highest priority), overvoltage, and then followed by transmission failure. A 6-bit fault code is shifted out of the RXD.

An overcurrent fault timing diagram is shown in [Figure 8](#).

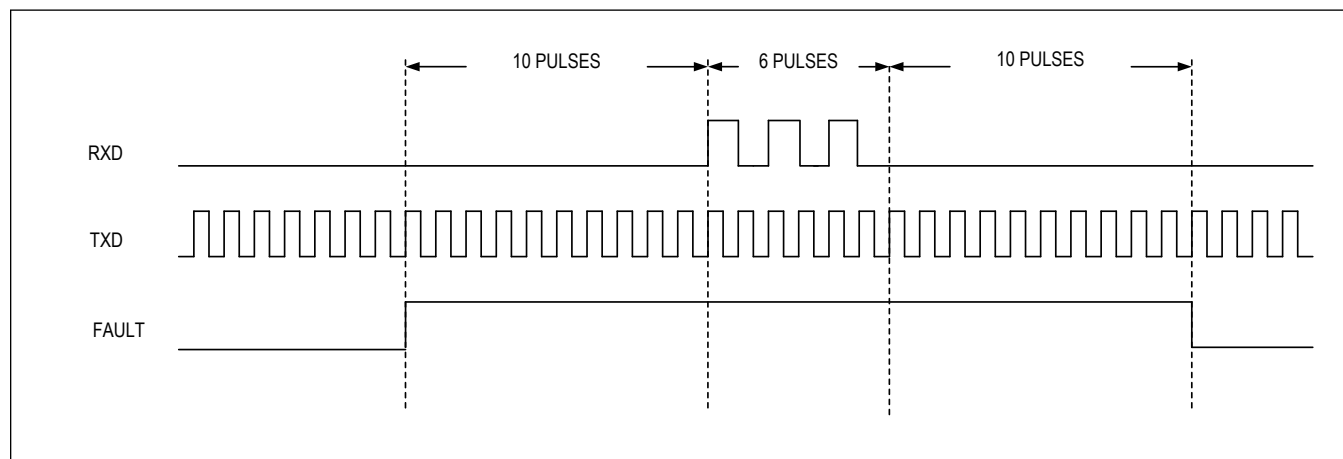


Figure 8. Overcurrent Fault Reporting Timing Diagram

Fault Clear

The fault condition can be cleared only after the 6-bit fault code is reported through RXD pin of the chip. Soon after the fault code is reported, send 10 clock pulses through the TXD pin, which will clear the fault condition, and de-asserts the fault pin. Fault detection is disabled after fault is cleared and another 100 low-to-high transitions are required on TXD to re-enable fault detection.

Standby Mode

Drive STBY pin high for standby mode, which switches the transmitter off and the receiver to a low current and low-speed state. The supply current is reduced to $60\mu\text{A}$ during standby mode. The bus line is monitored by a low differential comparator to detect and recognize a wakeup event on the bus line. Once the comparator detects a dominant bus level longer than t_{WAKE} , RXD is pulled low. Drive the STBY low for normal operation. Fault detection is disabled in standby mode.

Slow Slew Rate Mode

Connect a $26.1\text{k}\Omega$ resistor between ground and the STBY pin. This will put the device in slow slew rate mode where the typical rising slew rate is 10V/s and the typical falling slew rate is 18V/s , enabling the use of unshielded-twisted or parallel cable, compared with normal mode at $140\text{V}/\mu\text{s}$ falling and $180\text{V}/\mu\text{s}$ rising slew rate. The STBY pin voltage should be between 0.1V to 0.6V to remain in slow slew rate mode.

Silent Mode

Drive S high to place the device in silent mode. This disables the transmitter regardless of the voltage level at TXD. However, RXD is still active and monitors activity on the bus line.

MAX33011E/12E/14E/15E

+5V, 5Mbps CAN Transceiver with $\pm 65\text{V}$ Fault Protection, Fault Detection and Reporting, $\pm 25\text{V}$ CMR, and $\pm 45\text{kV}$ ESD Protection

Loopback Mode (MAX33014E, MAX33015E)

Drive LPBK high disables the transceiver (while keeping the CANH/CANL in recessive state), data is passed from TXD to RXD, and enables the local CAN controller to perform self diagnostic routines without disturbing the bus. When LPBK is connected to ground, the transceiver operates in normal mode.

Logic Compatibility (MAX33014E, MAX33015E)

A separate input V_L allows the devices to communicate with logic systems down to 1.62V while operating from a +5V supply. This provides a reduced input voltage threshold to the TXD, STBY, S, and LPBK inputs, and provides a logic-high output at RXD compatible with the microcontroller's supply rail. The logic compatibility eliminates an external logic level translator and longer propagation delay. Connect V_L to V_{DD} to operate with +5V logic systems.

For parts without the V_L pin, the logic supply pin is V_{DD} and all V_L references can be substituted with V_{DD} .

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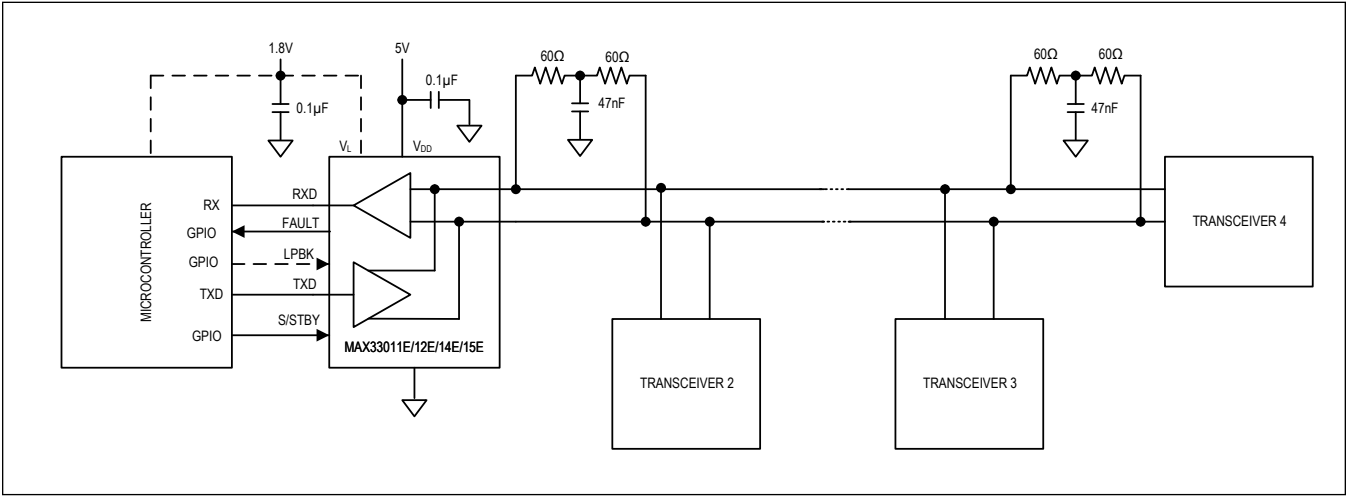
Applications Information

Reduced EMI and Reflections

In multidrop CAN applications, it is important to maintain a single linear bus of uniform impedance that is properly terminated at each end. A star, ring or tree configuration should never be used. Any deviation from the end-to-end wiring scheme creates a stub. High-speed data edges on a stub can create reflections back down to the bus. These reflections can cause data errors by eroding the noise margin of the system. Although stubs are unavoidable in a multidrop system, care should be taken to keep these stubs as short as possible, especially when operating with high data rates.

Typical Application Circuit

Multidrop CAN Bus



Ordering Information

PART NUMBER	TEMP RANGE	PIN-PACKAGE	FEATURE SET
MAX33011EASA+*	-40°C to +125°C	8 SO	S
MAX33012EASA+	-40°C to +125°C	8 SO	STBY
MAX33014EATB+*	-40°C to +125°C	10 TDFN	STBY, LPBK, VL
MAX33015EATB+*	-40°C to +125°C	10 TDFN	S, LPBK, VL

+ Denotes a lead(Pb)-free/RoHS-compliant package.

* Future product—contact factory for availability.

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Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	12/19	Initial release	—

For pricing, delivery, and ordering information, please visit Maxim Integrated's online storefront at <https://www.maximintegrated.com/en/storefront/storefront.html>.

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