

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP60T15G uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS(ON)}}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

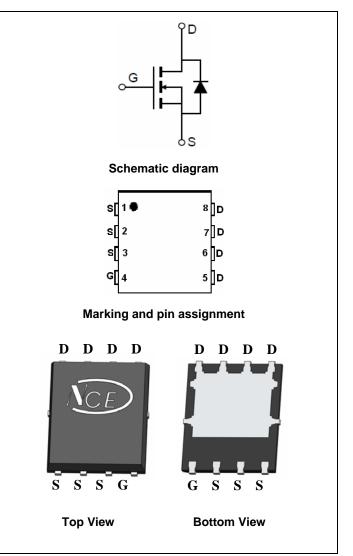
General Features

- V_{DS} =60V,I_D =150A
 - $R_{DS(ON)} < 3.1 \text{m}\Omega$ @ $V_{GS} = 10 \text{V}$ (Typ:2.8m Ω)
- Excellent gate charge x R_{DS(on)} product
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

100% UIS TESTED! 100% ΔVds TESTED!



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP60T15G	NCEP60T15G	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous (Silicon Limited)	I _D	150	А
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	105	Α
Pulsed Drain Current	I _{DM}	600	Α
Maximum Power Dissipation	P _D	200	W
Derating factor		1.6	W/°C
Single pulse avalanche energy (Note 5)	E _{AS}	819	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$ C



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NCEP60T15G

Thermal Characteristic

Thermal Resistance,Junction-to-Case ^(Note 2)	$R_{ heta JC}$	0.625	°C/W	Ì
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2.0	2.8	4.0	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =20A	-	2.8	3.1	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =20A	50	-	-	S
Dynamic Characteristics (Note4)	<u> </u>		•			
Input Capacitance	C _{lss}	\/ -20\/\/ -0\/	-	4500	-	PF
Output Capacitance	Coss	V_{DS} =30V, V_{GS} =0V, F=1.0MHz	-	965	-	PF
Reverse Transfer Capacitance	C _{rss}	r=1.0lvln2	-	24	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	6	-	nS
Turn-on Rise Time	t _r	V_{DD} =30 V , I_D =20 A	-	11	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =4.7 Ω	-	23	-	nS
Turn-Off Fall Time	t _f		-	3	-	nS
Total Gate Charge	Qg	V -20VI -20A	-	70	-	nC
Gate-Source Charge	Q_{gs}	$V_{DS}=30V,I_{D}=20A,$	-	18.6	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	15.3	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =150A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	150	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C$, $I_F = I_S$	-	50		nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	66		nC

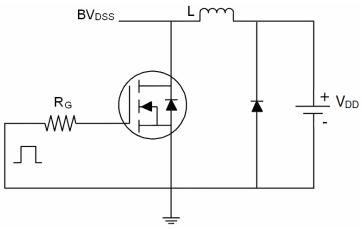
Notes:

- ${\it 1. Repetitive Rating: Pulse width limited by maximum junction temperature.}\\$
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,VDD=30V,VG=10V,L=0.5mH,Rg=25 Ω

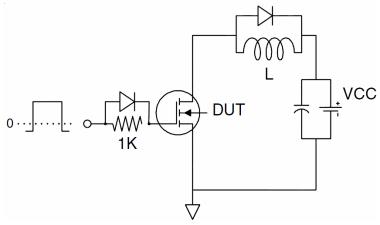


Test Circuit

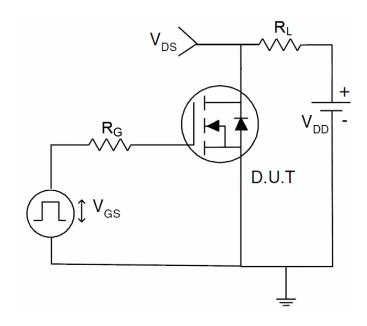
1) E_{AS} test Circuit



2) Gate charge test Circuit



3) Switch Time Test Circuit





Typical Electrical and Thermal Characteristics

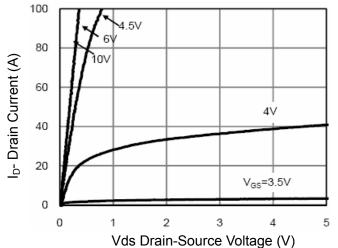


Figure 1 Output Characteristics

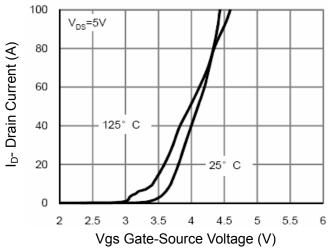


Figure 2 Transfer Characteristics

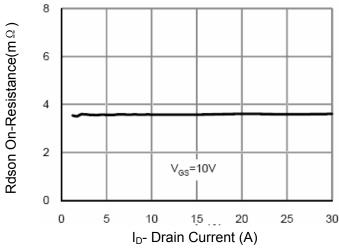


Figure 3 Rdson- Drain Current

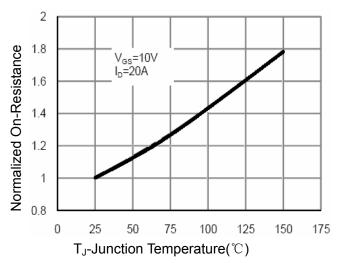


Figure 4 Rdson-Junction Temperature

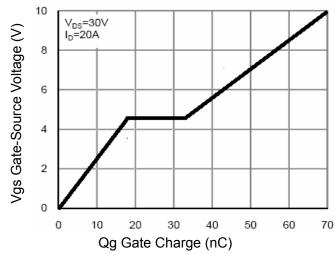


Figure 5 Gate Charge

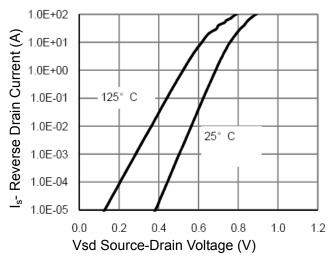


Figure 6 Source- Drain Diode Forward



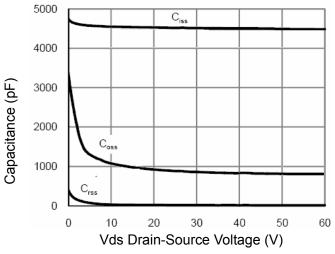


Figure 7 Capacitance vs Vds

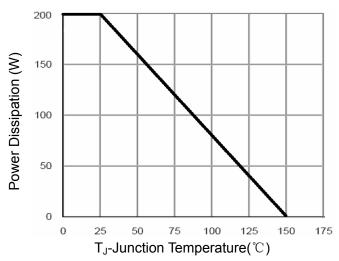


Figure 9 Power De-rating

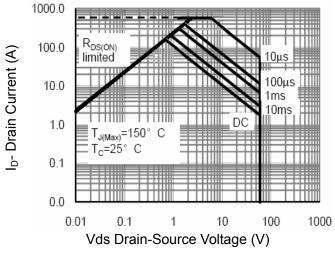


Figure 8 Safe Operation Area

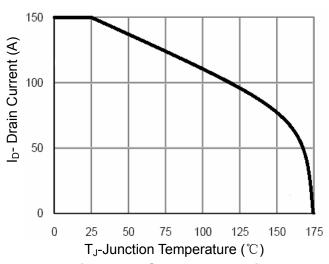


Figure 10 Current De-rating

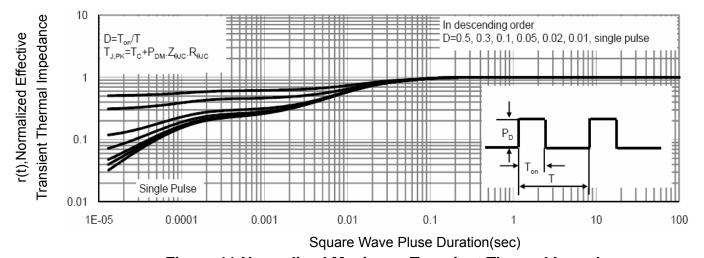
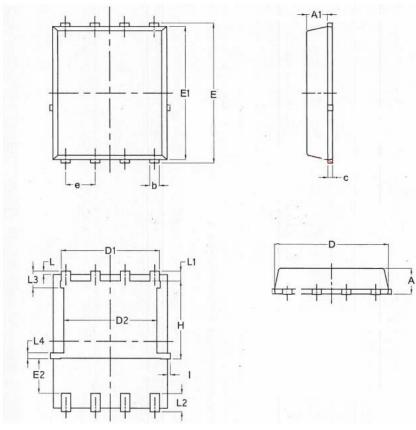


Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



Symbol	Dimensions In Millimeters			Dimensions In Inches			
•	Min.	Nom.	Max.	Min.	Nom.	Max.	
Α	0.90	1.10	1.17	0.0354	0.0433	0.0461	
A1	0.824	0.897	0.97	0.0324	0.0353	0.0382	
b	0.33	0.41	0.50	0.0130	0.0161	0.0197	
С	0.150	0.20	0.250	0.0059	0.0079	0.0098	
D	4.80	4.90	5.00	0.1890	0.1929	0.1969	
D1	3.91	4.22	4.36	0.1539	0.1661	0.1717	
D2	3.85	4.00	4.15	0.1516	0.1575	0.1634	
E	5.90	6.05	6.15	0.2323	0.2382	0.2421	
E1	5.65	5.76	5.85	0.2224	0.2268	0.2303	
E2	1.10	1	1	0.0433	1	1	
е		1.27 BSC			0.050 BSC		
L	0.05	0.15	0.25	0.0020	0.0059	0.0098	
L1	0.38	0.425	0.50	0.0150	0.0167	0.0197	
L2	0.51	0.785	0.86	0.0201	0.0309	0.0339	
L3	0.55	0.70	0.85	0.0217	0.0276	0.0335	
L4	0.10	0.25	0.40	0.0039	0.0098	0.0157	
Н	3.25	3.35	3.58	0.1280	0.1319	0.1409	
I	0	1	0.18	0	1	0.0071	

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NCEP60T15G

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