

20V N-Channel Trench MOSFET

Features

- Trench Power Technology
- Low R_{DS(ON)}
- Low Gate Charge
- Optimized for Fast-switching Applications

Applications

- Synchronous Rectification in DC/DC and AC/DC Converters
- Isolated DC/DC Converters in Telecom and Industrial

Product Summary

VDS 20V

 $R_{DS(ON)}$ (at V_{GS} =10V) < 20.5m Ω

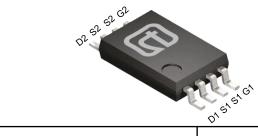
 $R_{DS(ON)}$ (at V_{GS} =4.5V) < 25m Ω

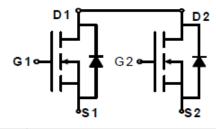
 $R_{DS(ON)}$ (at V_{GS} =2.5V) < 31.5m Ω

 I_D (at V_{GS} =10V) 6A

100% UIS Tested







Device	Package	Marking		
TTK8205A	TSSOP-8	8205A		

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Value	Unit			
Drain-Source Voltage (V _{GS} = 0V)		V_{DSS}	20	V		
Continuous Dunin Comment	$T_{\rm C} = 25^{\rm o}{\rm C}$	I _D	6	А		
Continuous Drain Current	$T_C = 70^{\circ}C$		4.8			
Pulsed Drain Current	I _{DM}	24	Α			
Gate-Source Voltage	V_{GSS}	±10	V			
Single Pulse Avalanche Energy	E _{AS}	7.4	mJ			
Avalanche Current	I _{As}	7	Α			
Dawar Dissipation (note	3) $\frac{T_C = 25^{\circ}C}{T_C = 70^{\circ}C}$	Б	1.5	W		
Power Dissipation (note	$T_{\rm C} = 70^{\circ}{\rm C}$	P _D	0.96	W		
Operating Junction and Storage Tempe	T _J , T _{stg}	-55~+150	∘C			

Thermal Resistance						
Parameter	Symbol	Value	Unit			
Thermal Resistance, Junction-to-Case	R _{thJC}	14.4	0000			
Thermal Resistance, Junction-to-Ambient	R _{thJA}	83	°C/W			



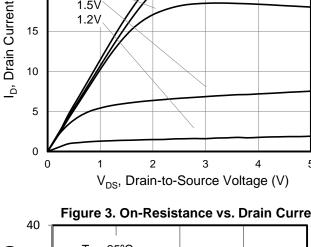
			Value				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			<u>!</u>	!			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_{D} = 250\mu A$	20			V	
7 0 1 1/1 5 1 0		$V_{DS} = 20V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20V, V_{GS} = 0V, T_{J} = 70^{\circ}C$			25		
Gate-Source Leakage	$I_{\rm GSS}$	$V_{GS} = \pm 10V$			±100	nA	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	0.5	0.7	1.2	٧	
		$V_{GS} = 10V, I_{D} = 3A$		17.0	20.5	mΩ	
Drain-Source On-Resistance (Note3)	$R_{DS(on)}$	$V_{GS} = 4.5V, I_{D} = 3A$		19.5	25	mΩ	
		$V_{GS} = 2.5V, I_{D} = 3A$		25	31.5	mΩ	
Forward Transconductance (Note3)	g_{fs}	$V_{DS} = 5V$, $I_{D} = 3A$	7			S	
Dynamic							
Input Capacitance	C _{iss}	V 0V		466		pF	
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 10V,$		65			
Reverse Transfer Capacitance	C_{rss}	f = 1.0MHz		58			
Total Gate Charge	Q_g			5.7			
Gate-Source Charge	Q_gs	$V_{DD} = 10V, I_{D} = 6A, V_{GS} = 4.5V$		0.8		nC	
Gate-Drain Charge	Q_{gd}	93		1.4			
Turn-on Delay Time	$t_{d(on)}$			15		ns	
Turn-on Rise Time	t _r	$V_{DD} = 10V, I_{D} = 6A,$		17			
Turn-off Delay Time	t _{d(off)}	$R_G = 2.5\Omega$		42			
Turn-off Fall Time	t _f			40			
Drain-Source Body Diode Characteris	stics		_				
Continuous Body Diode Current	I _S	T 0500			6	A	
Pulsed Diode Forward Current	I _{SM}	$T_{\rm C} = 25^{\rm o}{\rm C}$			24		
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 6\text{A}, V_{GS} = 0\text{V}$			1.2	V	

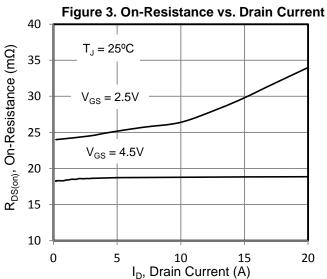
Notes

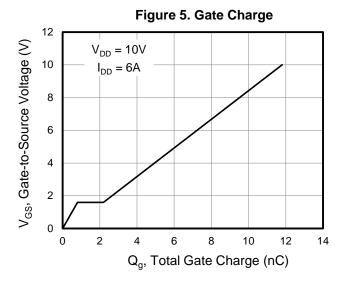
- 1. Repetitive Rating: Pulse Width limited by maximum junction temperature
- 2. V_{DD} = 20V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 1%

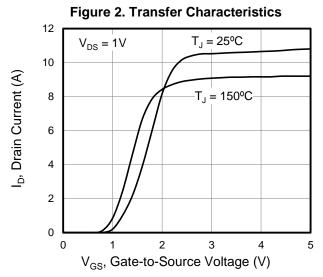
Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

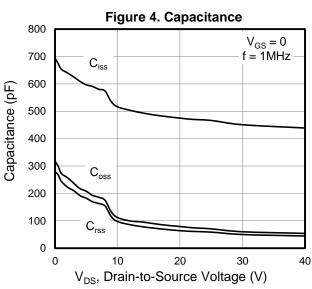
Figure 1. Output Characteristics 25 5V 2.5V I_D, Drain Current (A) 20 1.8V 1.5V 1.2V 15 10 5 0 3 0 5

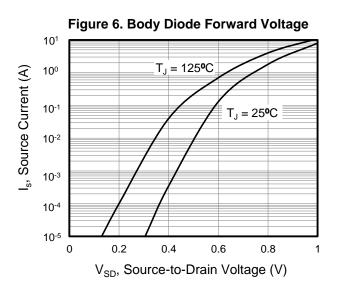












Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted



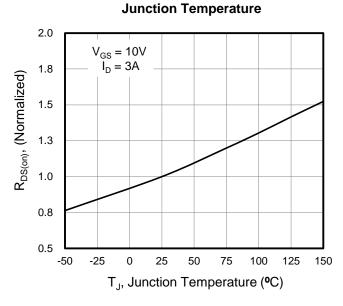


Figure 8. Threshold Voltage vs. Junction Temperature

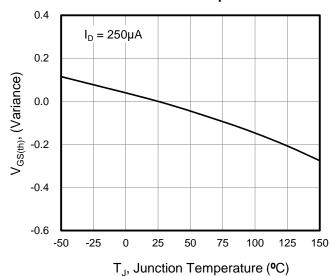


Figure 9. Transient Thermal Impedance

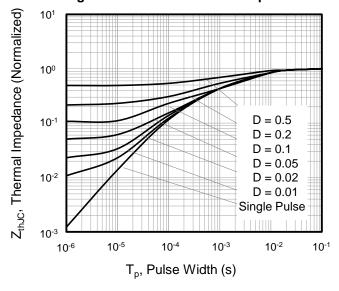


Figure 10. Safe operation area

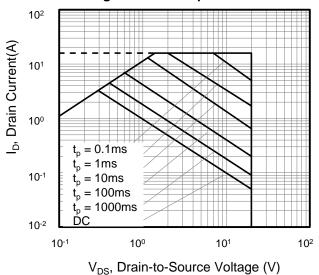


Figure A: Gate Charge Test Circuit and Waveform

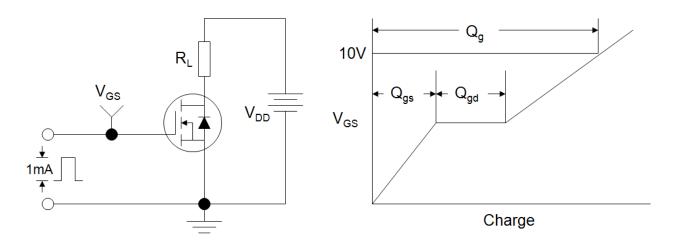


Figure B: Resistive Switching Test Circuit and Waveform

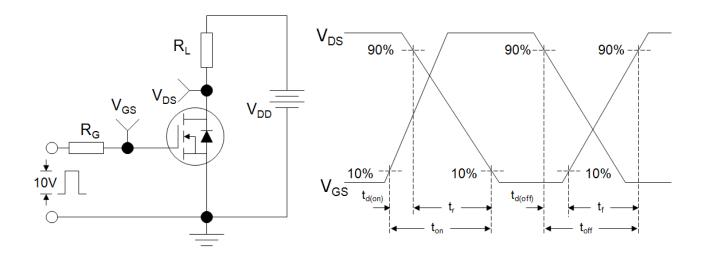
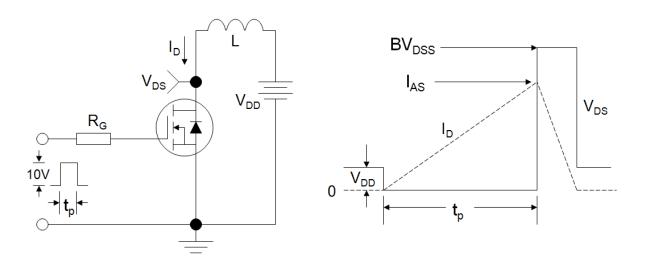
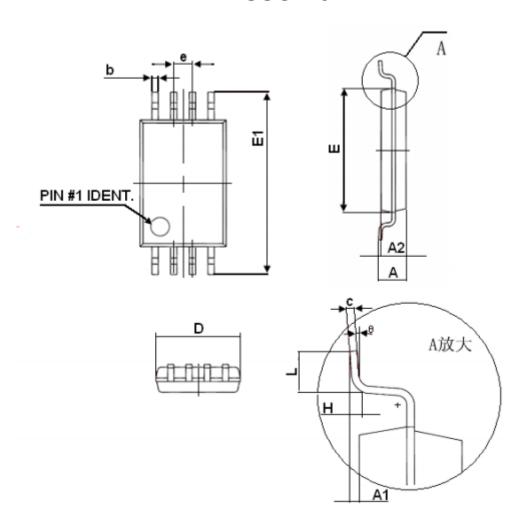


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TSSOP-8



Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbo1	Min	Max	Min	Max	
D	2.900	3. 100	0.114	0.122	
E	4.300	4. 500	0.169	0. 177	
ь	0.190	0.300	0.007	0.012	
С	0.090	0.200	0.004	0.008	
E1	6.250	6. 550	0.246	0.258	
A		1. 200		0.047	
A2	0.800	1.000	0.031	0.039	
A1	0.050	0. 150	0.002	0.006	
e	0.65 (BSC)	0.026	(BSC)	
L	0.500	0. 700	0.020	0.028	
Н	0.25(T	0.25(TYP)		TYP)	
θ	1°	7°	1°	7°	



Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Unigroup does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Unigroup.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling Wuxi Unigroup products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Unigroup for any damages arising or resulting from such use or sale.

Wuxi Unigroup disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Unigroup's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Unigroup Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Unigroup products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only. It is not guaranteed for volume production. Wuxi Unigroup believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.